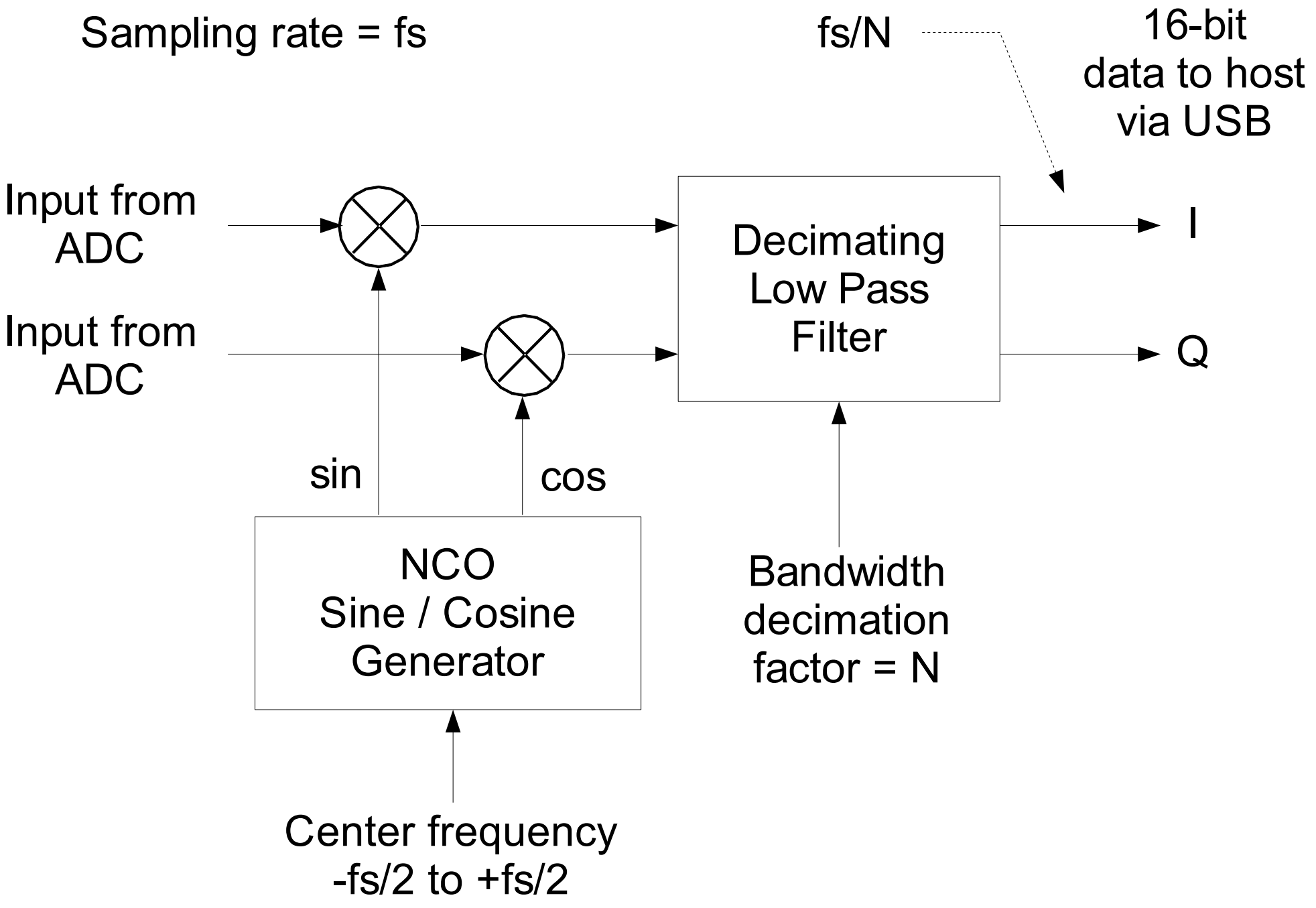
Sampling rate = fs
fs/N
16-bit
data to host
via USB
Input from
ADC
Input from
ADC
Decimating
Low Pass
Filter
I
Q
sin
cos
NCO
Sine / Cosine
Generator
Bandwidth
decimation
factor = N
Center frequency
-fs/2 to +fs/2