GPS Antennas RF Design Considerations for u-blox GPS Receivers Application Note

Abstract

Overview of important antenna and interference issues to be considered when integrating u-blox GPS receivers.





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1 Introduction

Antennas are a critical part of any GPS receiver design and their importance cannot be stated highly enough.

Even the best receiver cannot bring back what has been lost due to a poor antenna, in-band jamming, or a bad RF-board design. GPS signals are extremely weak and present unique demands on the antenna. The choice and implementation of the antenna can ultimately play a significant role in GPS performance.

This document considers some of the RF and interference issues when implementing a GPS antenna.

2 Antenna basics

2.1 General considerations

A GPS receiver needs to receive signals from as many satellites as possible. Optimal performance will not be available in narrow streets and underground parking lots or if objects cover the antenna. Poor visibility may result in position drift or a prolonged Time-To-First-Fix (TTFF). **Good sky visibility is therefore an important advantage.** A GPS receiver will only achieve the specified performance if the average carrier to noise power density ratio (C/N_0) of the strongest satellites reaches at least 44 dBHz. In a well-designed system, the average of the C/N_0 ratio of high elevation satellites should be in the range between 44 dBHz and about 50 dBHz. With a standard off-the-shelf active antenna, 47 dBHz should easily be achieved.

2.2 Antenna requirements

For optimal performance, use antennas with high gain (e.g. >4 dBic) and active antennas with an LNA with a low noise figure (<2dB). Ideally, the antenna has:

- A low level of directivity¹ (see Figure 1).
- Good antenna visibility to the sky.
- Good matching between antenna and cable impedance.
- High gain.
- Filter.

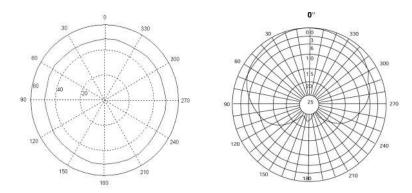


Figure 1: Antenna radiation pattern showing low directivity (left) vs high directivity (right)



Appendix A provides an example of how to estimate the influence of the noise figure (NF) and gain on antenna performance.

¹ Antenna gain increases with the level of directivity.



2.3 Antenna placement

The position of the antenna mounting is crucial for an optimal performance of the GPS receiver. When using patch antennas, the antenna plane should be parallel to the geographic horizon. The antenna must have full view of the sky ensuring a direct line-of-sight with as many visible satellites as possible.

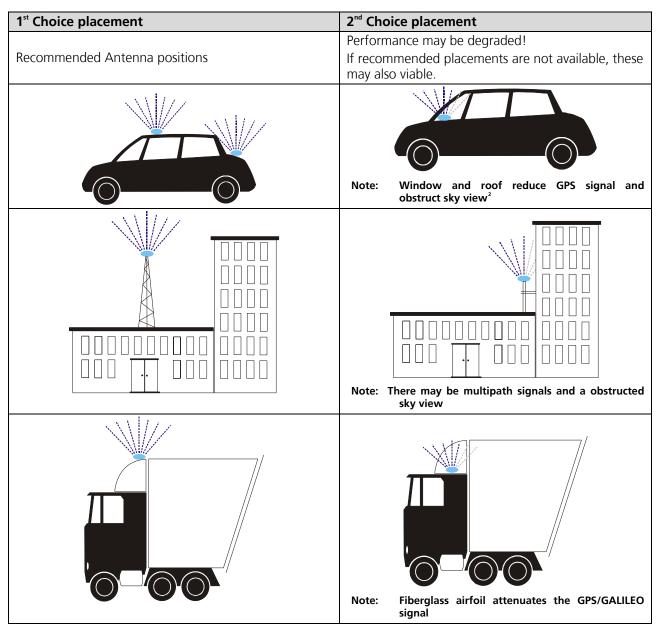


Figure 2: Recommended antenna position



Place the antenna as far away as possible from radiating or jamming signals.

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² Some cars have a metallic coating on the windscreens. GPS/GALILEO reception may not be possible in such a car without the use of SuperSense® Technology. There is usually a small section, typically behind the rear view mirror, reserved for mobile phone and GPS/GALILEO antennas



2.4 Active and passive antennas

Passive antennas contain only the radiating element, e.g. the ceramic patch or the helix structure. Sometimes they also contain a passive matching network to match the electrical connection to 50 Ohms impedance. Active antennas have an integrated Low-Noise Amplifier (LNA). This is beneficial in two respects. First, the losses of the cable after the LNA no longer affect the overall noise figure of the GPS receiver system. Secondly, the LNA in the antenna helps to reduce the overall noise figure of the system resulting in a better sensitivity. Some receivers are designed such that they will only work with active antennas.

Active antennas need a power supply that will contribute to GPS system power consumption, typically in the order of 3 to 20 mA. Usually, the supply voltage is fed to the antenna through the coaxial RF cable. Inside the antenna, the DC component on the inner conductor will be separated from the RF signal and routed to the supply pin of the LNA.

The use of an active antenna is always advisable if the RF-cable length between the receiver and antenna exceeds about 10 cm. Care should be taken that the gain of the LNA inside the antenna does not lead to an overload condition at the receiver. For receivers that also work with passive antennas an antenna LNA gain of 15 dB is usually sufficient, even for cable lengths up to 5 m. There's no need for the antenna LNA gain to exceed 26 dB for use with u-blox receivers (at the RF input). With shorter cables and a gain above 35 dB, an overload condition might occur on some receivers.

When comparing gain figures of active and passive antennas one has to keep in mind that the gain of an active antenna is composed of two components, the antenna gain of the passive radiator, given in dBic, and the LNA power gain given in dB. A low antenna gain cannot be compensated by high LNA gain. It is not possible to judge the quality of the antenna if a manufacturer provides one total gain figure. One would need information on antenna gain (in dBic), amplifier gain, and amplifier noise figure.

Ac	tive antenna	Passive antenna		
_	Needs more power (10 – 60 mW) than a passive	+	Does not add anything to the power budget	
	antenna	_	Antenna must be connected with a carefully	
+	Is more tolerant to minor impedance miss-match or cable length than passive antenna (see section 5.3).		designed micro strip or strip line of maximum 10 cm to the GPS receiver to ensure good GPS performance.	
+	Helps to keep the receiver noise figure low.	_	Jamming signals coupled into the micro-strip or	
+	Is less affected by jamming into the antenna		strip line negatively affect the performance.	
	cable than a passive antenna (if equipped with	_	RF design experience is required to properly	
	filter).		design a passive antenna.	

Table 1: Active vs. passive antenna



3 Passive antenna types

The GPS signal is right-hand circular polarized (RHCP). This results in a style of antenna that is different from the well-known whip antennas used for linear polarized signals.

3.1 Patch antenna

The most common antenna type for GPS applications is the patch antenna. Patch antennas are flat, generally have a ceramic and metal body and are mounted on a metal base plate. They are often cast in a housing.

Patch antennas are ideal for situations where the antenna is mounted on a flat surface, e.g. the roof or the dashboard of a car. Patch antennas can show a very high gain, especially if they are mounted on top of a large ground plane (70 x 70 mm). Ceramic patch antennas are very popular because of the low costs and the huge variation of available sizes (40 x 40 mm down to 10 x 10 mm; typical 25 x 25 mm).

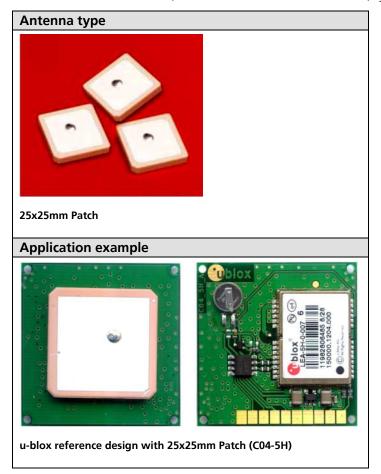


Figure 3: Examples of patch antennas

A smaller antenna will present a smaller aperture to collect the signal energy from sky resulting in a lower overall gain of the antenna. This is the result of pure physics and there is no "magic" to get around this problem. Amplifying the signal after the antenna will not improve the signal to noise ratio.



(F

Patch antennas of 25mm by 25mm show optimal performance and are cost-efficient. Patches smaller than 17mm by 17mm tend to demonstrate moderate navigation performance³. Performance is dependant on the ground plane size.

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3.2 Helix antenna



Figure 4: Helix antenna

Another style is the quadrifilar helix antenna. The actual geometric size depends on the dielectric that fills the space between the active parts of the antenna. If the antenna is only loaded with air it will be comparatively large (60 mm length and 45 mm diameter), high dielectric constant ceramics result in a much smaller form factor. The smaller the dimensions of the antenna, the more performance-critical tight manufacturing tolerances become.

As with patch antennas, filling the antenna with a high dielectric constant material can reduce the size of helix antennas. Sizes in the order of 18 mm length and 10 mm diameter are being offered to the market.

Again, antenna gain will decrease with decreasing size of the antenna.

(B)

Helical antennas are typically used in applications where multiple antenna orientations are possible. They are robust and demonstrate good navigation performance.

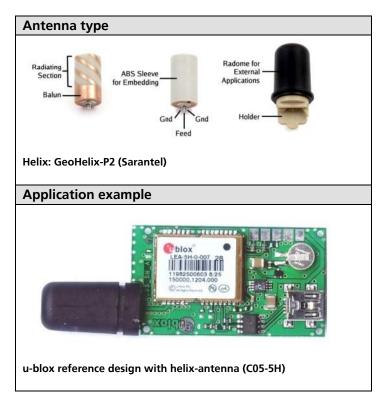


Figure 5: Examples of helix antenna



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³ Unless enhanced by u-blox SuperSense® technology.



3.3 Monopole antennas

3.3.1 Chip antenna

Chip antennas are becoming increasingly important for GPS designs. Their low cost and extremely small size (down to 3.2 x 1.6 x 1.1 mm), as well as high gain and omni-directional radiation patterns make them particularly attractive in consumer electronic applications such as mobile telephones and PNDs. Due to their miniature size, a variety of factors influence the performance of chip antennas. These factors include the footprint, ground plane size, isolation distance (typical 5 mm) and mounting of the chip antenna and GPS device. The isolation distance or "keep-out area" can have an important impact on antenna efficiency, and thus on GPS performance, and needs to be carefully considered in designs. Isolation distances must be included to avoid deviations in antenna performance. Even with these measures acceptable performance cannot always be guaranteed due to potential detuning effects created by nearby objects.

Even if some antenna manufacturers claim that a ground plane is not required, the available ground plane has a significant impact on the GPS performance of a chip antenna. Therefore, not only the size of the chip but also the ground plane must be considered in the design. For designs with a sufficiently large ground plane a chip antenna can provide satisfactory GPS performance. However, in designs with an inadequate ground plane and device layout their performance is insufficient for GPS.

Chip antennas have a 3dB loss compared to helical or patch antennas due to linear polarization and their performance is highly dependant on the size of the ground plane.

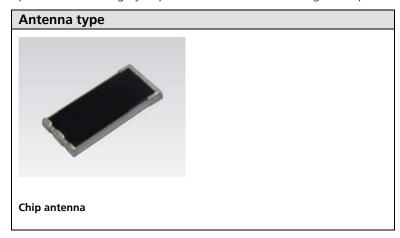


Figure 6: Example of chip antenna

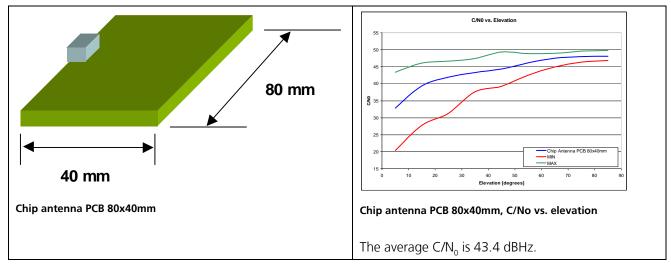


Figure 7: Performance of chip antenna with 80 x 40 cm PCB



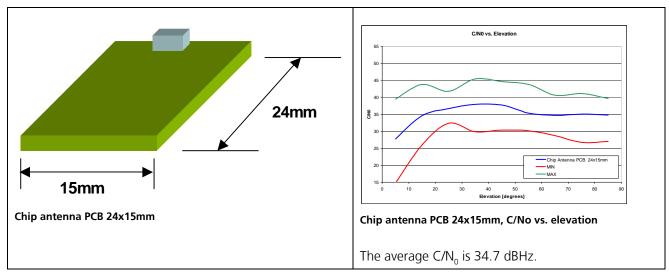


Figure 8: Comparison of patch and chip antennas

Figure 8 shows a performance comparison between a 25x25 mm patch antenna as reference and two chip antennas.

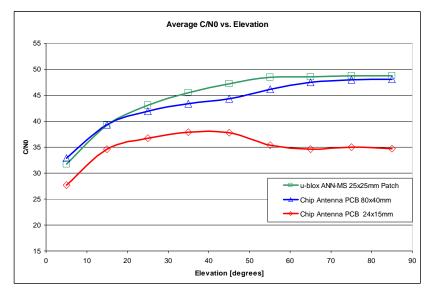


Figure 9: Comparison of 25x25 mm patch antenna with chip antennas

Chip antennas are not recommended for use in devices where navigation is an essential feature.

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Typ. Gain [dBic]

1.0



A linear polarized whip or a PCB strip antenna is a simple and economical antenna solution if the user is willing to accept significantly weaker signals. Compared to a patch or helix these antennas have additional losses due

- Polarization mismatch of about -3 dB due to linear polarization.
- Lower signals due to massively in-homogenous sensitivity pattern (directivity)
- Lower overall gain

The PCB strip is perhaps the cheapest way to implement a GPS antenna, but has some definite drawbacks which must be considered. Depending on the geometry chosen, the antenna has a high directivity. PCB antennas are typically bigger than Chip antennas and usually have a larger bandwidth than Chip or Patch antennas. In addition, implementing PCB strip antennas requires RF expertise.

100 x 30

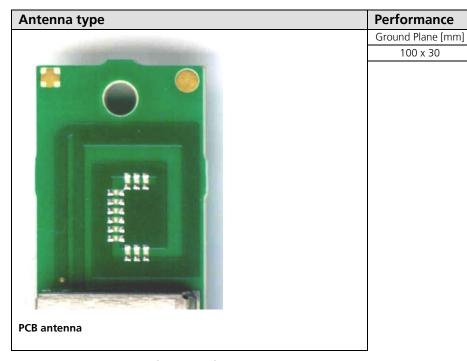


Figure 10: Example and performance of PCB antenna



PCB antennas are not recommended for use in devices where navigation is an essential feature.



3.3.2 Fractal Element Antenna (FEA)

A fractal antenna is an antenna that uses a self-similar design to maximize the length, or increase the perimeter on inside sections or the outer structure, of material that can receive or transmit electromagnetic signals within a given total surface area.

Fractal antennas have a 3dB loss compared to helical or patch antennas due to linear polarization and their performance is highly dependant on the size of the ground plane.

Antenna type	Performance	
	Ground Plane [mm]	Typ. Gain [dBic]
	None	n/a
	50 x 30	-1.2
	60 x 30	1.5
	60 x 35	0.2
FEA antenna	120 x 65	2.2 to 2.5

Figure 11: Example and performance of FEA antenna

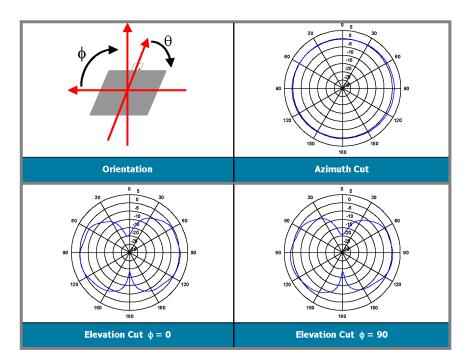


Figure 12: Radiation pattern



FEA antennas are not recommended for use in devices where navigation is an essential feature.



3.4 Dipole antenna

Dipole antennas can be a very cost effective solution, especially when printed on a PCB. They show acceptable performance in indoor environments. The field does not depend on a ground plane.

Dipole antennas are linear, not circular polarized. This results in a 3dB loss in open space for GPS but has some advantage for the backlobe, which is useful for indoor reception. Dipole antennas demonstrate similar drawbacks as PCB antennas and require RF expertise.

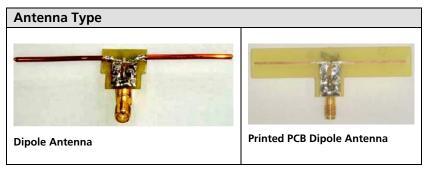


Figure 13: Examples and performance of dipole antennas

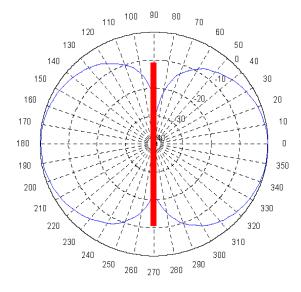


Figure 14: High directivity (virtually no reception perpendicular to antenna orientation)



Dipole antennas are not recommended for use in devices where navigation is an essential feature.



3.5 Loop antenna

The loop antenna shown in Figure 15 is printed on a sticker, which is attached to a windshield. Since its field is independent of a ground plane, its impedance and center frequency are not very sensitive to objects in the near field

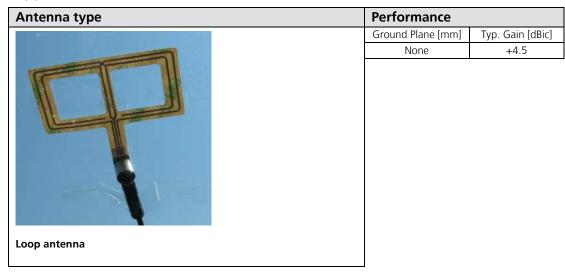


Figure 15: Example and performance of loop antenna



If mounted on glass as specified, loop antennas demonstrate good navigation performance.

3.6 Planar Inverted F Antenna (PIFA)

The PIFA antenna literally looks like the letter 'F' lying on its side with the two shorter sections providing feed and ground points and the 'tail' (or top patch) providing the radiating surface. PIFAs make good embedded antennas in that they exhibit a somewhat omni directional pattern and can be made to radiate in more than one frequency band. They are linear polarized and their efficiency is only moderate.

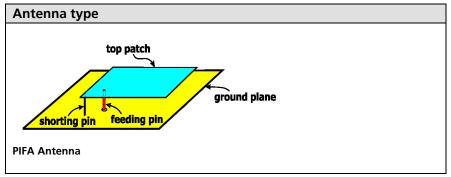


Figure 16: Example of PIFA antenna



PIFAs are mainly used in cellular phones (E-911). Their use in devices where navigation is an essential feature is not recommended.



3.7 High end GPS antennas

For precision applications such as surveying or timing, some very high-end systems exist. Common to these designs are large size, high power consumption and high price. These designs are highly optimized to suppress multi-path signals reflected from the ground (choke ring antennas, multi-path limiting antennas, MLA). Another area of optimization is accurate determination of the phase center of the antenna. For precision GPS applications with position resolution in the millimeter range it is important that signals from satellites at all elevations virtually meet at exactly the same point inside the antenna. For this type of application receivers with multiple antenna inputs are often required.



4 Which antenna is best for my application?

Helix and Patch antennas are the most widely used types in GPS applications. This chapter examines some of the considerations to be made when selecting the antenna type.



Always test the actual performance of different antenna types in a real life environment before beginning the mechanical design of the GPS enabled product.

4.1 Helix or patch?

For practical applications the possibilities of integrating a certain style of antenna into the actual device is of primary concern. Some designs naturally prefer the patch type of antenna, e.g. for rooftop applications. Others prefer the pole like style of the helix antenna, which is quite similar to the style of mobile phone antennas. Furthermore, it is important that the antenna's main lobe points to the sky in order to receive as many satellites as possible with the maximum antenna gain. If the application is a hand held device, the antenna should be designed in a way that natural user operation results in optimum antenna orientation. The helix antenna seems to be more appropriate in this respect.

However, one has to keep in mind that comparable antenna gain requires comparable size of the antenna aperture, which will lead to a larger volume filled by a helix antenna in comparison to a patch antenna. Helix antennas with a "reasonable" size will therefore typically show a lower sensitivity compared to a "reasonably" sized patch antenna.

A helix antenna might result in a "more satellites on the screen" situation in difficult signal environments when directly compared with a patch antenna. This is due to the fact that the helix will more easily pick up reflected signals through its omni directional radiation pattern. However, the practical use of these signals is very limited because of the uncertain path of the reflected signals. Therefore, the receivers can see more satellites but the navigation solution will be degraded because of distorted range measurements in a multi-path environment.

Helix antennas	Patch antennas		
+ omnidirectional	+ high gain		
+ robust	+ low cost		
– cost	+ large variety of sizes available		
 space requirements 	on market		
	 Less isolation between feed and antenna when compared to helix antenna 		

Table 2: Helix vs. Patch Antennas

4.2 Other antenna types

In devices, where navigation is not a core feature, chip, fractal, PCB, PIFA or dipole antennas can be an alternative to patch or helical antennas due to their small size and/or low costs. It is important to understand that these antennas do not provide the reception quality of patch or helical antennas. For this reason using patch or helical antennas is recommended in applications where navigation performance matters.



5 Design considerations

5.1 Patch antennas

Patch antennas are ideal for an application where the antenna sits on a flat surface, e.g. the roof of a car. Patch antennas can demonstrate a very high gain, especially if they are mounted on top of a large ground plane. Ceramic patch antennas are very popular because of their small size, typically measuring 25 x 25 mm² down to 10x10 mm². Very cheap construction techniques can use ordinary circuit board material like FR-4 or even air as a dielectric, but this will result in a much larger size, typically in the order of 10 x 10 cm². Figure 17 shows a typical example of the radiation pattern of a 16 x 16 mm² ceramic patch antenna. This measurement only shows the upper sphere of the radiation pattern. Depending on ground plane size there will also be a prominent back lobe present.

Directivity (YZ) - Ground plane size

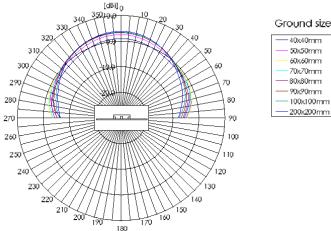


Figure 17: Typical Radiation Pattern of a Patch Antenna

5.1.1 Ground plane

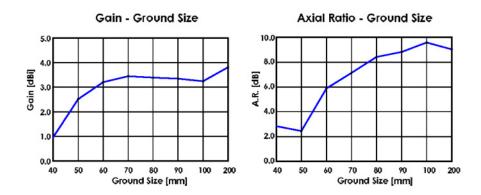


Figure 18: Typical gain and axial ratio of a patch antenna with respect to ground plane size



For the specific example shown in Figure 18 one can easily see that the so-called axial ratio, the relation of major to minor axis of the elliptical polarization has a minimum at the 50 mm² square ground plane. At this point, the polarization of the antenna is closest to an ideal circular polarization (axial ratio = 0 dB). At a 100 mm² square ground plane size this particular patch shows an axial ratio in the order of 10 dB, which is closer to linear polarization than to circular and will result in respective losses. This effect can also be seen in the left graph of the figure, where gain no longer increases with increasing ground plane size. In conclusion, the correct dimensions for the size of the ground plane can serve as a useful compromise between maximum gain and reasonable polarization loss.

A good allowance for ground plane size is typically in the area of 50 to 70 mm². This number is largely independent of the size of the patch itself (when considering ceramic patches). Patch antennas with small ground planes will also have a certain back-lobe in their radiation pattern, making them susceptible to radiation coming from the backside of the antenna, e.g. multi-path signals reflected off the ground. The larger the size of the ground plane, the less severe this effect becomes.

Smaller sized patches will usually reach their maximum gain with a slightly smaller ground plane compared to a larger size patch. However, the maximum gain of a small sized patch with optimum ground plane may still be much lower than the gain of a large size patch on a less than optimal ground plane.

Figure 19 illustrates the impact of ground plane size, patch size and patch thickness on the antenna gain.

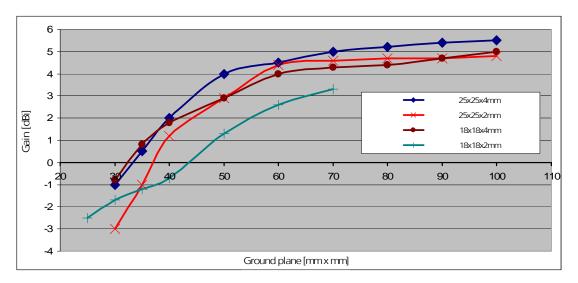


Figure 19: Antenna gain vs. ground plane



It is not only the gain and axial ratio of the patch antenna that is affected by the size of the ground plane but also the matching of the antenna to the 50 Ohms impedance of the receiver.



5.1.2 Placement

The performance of a patch antenna heavily depends on the size, shape and symmetry of the ground plane. Improper placement of the patch will yield poor antenna performance and strong directivity (see Figure 20).

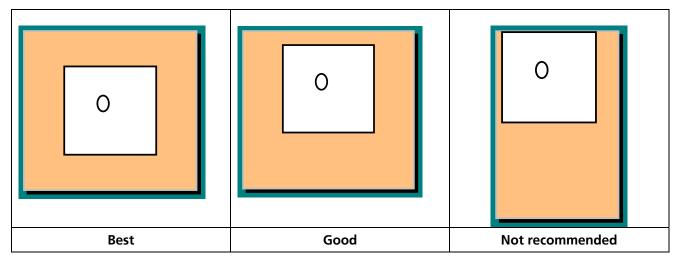


Figure 20: Placement of Patch Antenna

In addition the following placement issues need to be taken into account for patch antennas:

- No components should be placed close to the patch antenna.
- Maintain a minimum distance between the antenna and the housing of the device.
- No signal lines should pass under or near the antenna.

5.1.3 ESD issues



GPS receivers are sensitive to Electrostatic Discharge (ESD). Special precautions are required when handling.

Most defects caused by ESD can be prevented by following strict ESD protection rules for production and handling. When implementing passive antenna patches or external antenna connection points, then additional ESD measures as shown in Figure 21 can also avoid failures in the field.

Small passive antennas (<2 dBic and performance critical)	Passive antennas (>2 dBic or performance sufficient)	Active Antennas
LNA with appropriate ESD rating	RF_IN GPS Receiver	GPS Receiver

Figure 21: ESD precautions



Protection measure A is preferred due to performance and protection level considerations.



5.2 Helix antennas

Helix antennas can be designed for use with or without ground plane. If a helix antenna is designed without ground plane it can be tuned such to show a more omni directional radiation pattern as shown in Figure 22.

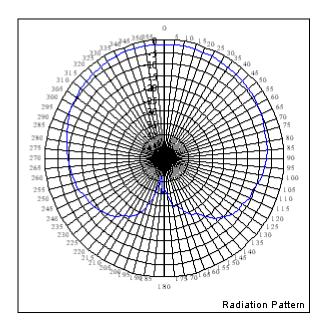


Figure 22: Radiation pattern of helix antenna without ground plane (Sarantel)

Although we can determine an axial ratio close to 9 dB between zero degree and 90 degrees elevation, which compares to the patch antenna, the back lobe of the helix generally degrades much smoother and does not show any sensitivity at the –180 degree direction. In contrast, the back lobe of a patch antenna depends very much on size and shape of the ground plane.

5.2.1 Ground plane

Helix antennas typically do not require ground planes. However, a ground plane can significantly improve performance (e.g. 2-3 dB).

Due to antenna near field radiation patterns, currents can be induced in ground planes in close proximity to the antenna. This can negatively affect the radiation pattern of the antenna and overall performance, and additionally increases the antenna's susceptibility to hand loading and interference. For this reason ground planes should be kept a minimum distance from the radiating section of a helix antenna. See the recommendations of the manufacturer for more information (e.g. Sarantel).

5.2.2 Placement

Helix antennas can either be placed internally in the GPS device or used as a free space antenna.

With free space antennas ground planes within 5mm of the antenna radiating section can affect the performance of the antenna. In addition mechanical supports should be provided to hold the antenna in place. Certain system parts around the antenna, for example PCB or LCD screen, may lead to impairment of the antenna radiation pattern



5.3 Antenna matching

All common GPS/GALILEO antennas are designed for a 50 Ohms electrical load. Therefore, one should select a 50 Ohms cable to connect the antenna to the receiver. However, there are several circumstances under which the matching impedance of the antenna might shift considerably. Expressed in other words, this means that the antenna no longer presents a 50 Ohms source impedance. Typically what happens is that the center frequency of the antenna is shifted away from GPS/GALILEO frequency - usually towards lower frequencies – by some external influence. The reasons for this effect are primarily disturbances in the near field of the antenna. This can either be a ground plane, that does not have the size for which the antenna was designed, or it can be an enclosure with a different dielectric constant than air.

In order to analyze effects like this one would normally employ electrical field simulations, which will result in exact representation of the electric fields in the near field of the antenna. Furthermore, these distortions of the near field will also show their effect in the far field, changing the radiation pattern of the antenna. Unfortunately, there is no simple formula to calculate the frequency shift of a given antenna in any specified environment. So one must do either extensive simulation or experimental work. Usually, antenna manufacturers offer a selection of pre-tuned antennas, so the user can test and select the version that best fits the given environment. However, testing equipment such as a scalar network analyzer is recommended to verify the matching.

Again, it must be pointed out that the smaller the size of the antenna, the more sensitive it will be to distortions in the near field. Also the antenna bandwidth will decrease with decreasing antenna size, making it harder to achieve optimum tuning.

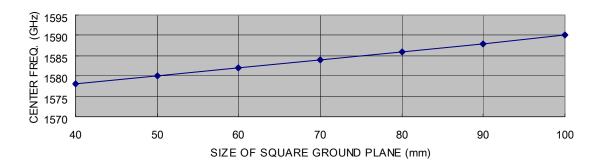


Figure 23: Center frequency of a 25 x 25 mm² patch vs. ground plane dimension

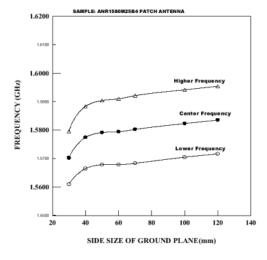


Figure 24: Dependency of center frequency on ground plane dimension for a 25 x 25 mm² patch



An LNA placed very close to the antenna can help to relieve the matching requirements. If the interconnect length between antenna and LNA is much shorter than the wavelength (9.5 cm on FR-4), the matching losses become less important. Under these conditions the matching of the input to the LNA becomes more important. Within a reasonable mismatch range, integrated LNAs can show a gain decrease in the order of a few dBs versus an increase of noise figure in the order of several tenths of a dB. If your application requires a very small antenna, an LNA can help to match the impedance of the antenna to a 50 Ohms cable. This effect is indeed beneficial if the antenna cable between the antenna and the receiver is only short. In this case, there's no need for the gain of the LNA to exceed 10-15 dB. In this environment the sole purpose of the LNA is to provide impedance matching and not signal amplification.

5.4 GSM applications

GSM uses power levels up to 2W (+33dBm). The absolute maximum power input at the GPS receiver is typically -5 dBm.

5.4.1 Isolation between GPS and GSM antenna

For GSM applications plan a minimum isolation of 40dB. In a handheld type design an isolation of approximately 20dB can be reached with careful placement of the antennas, but this isn't sufficient. In such applications an additional input filter is needed on the GPS side to block the high energy from the GSM transmitter. Examples of these kinds of filters would be a SAW Filter from Epcos (B9444) or Murata.

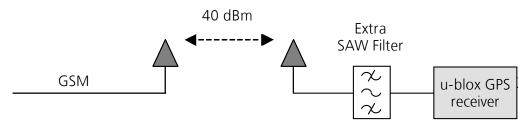


Figure 25: GPS and GSM antenna isolation

5.5 Dual antenna systems

Dual antenna systems are typically used to switch between internal antennas and external active antennas. Figure 26 and Figure 27 give examples of dual antenna systems.

The following points need to be considered for the design:

- Most switches require DC blockers at every input, and when selecting the switch or Schmitt trigger particular attention needs to be give to the switching thresholds function of the antenna being used. Depending on the switch an additional buffer may be necessary.
- The LNA is placed before the SAW filter because LNAs can accept a maximum power of between 10 to 15dBm.
- Placing the LNA after the SAW filter will reduce sensitivity by ~1dBm but maximum input power will be higher (typically 25dBm).
- It is recommended to power the LNA through VCC RF.
- Selection of the ESD diode depends on the voltage supply (See the Integration manual for the specific u-blox receiver).
- The antenna detection circuitry is the same as that in the Integration manual for the specific u-blox receiver.
- Open Circuit Detection must be activated on the module so that the antenna supply can be switched off when there is no antenna.



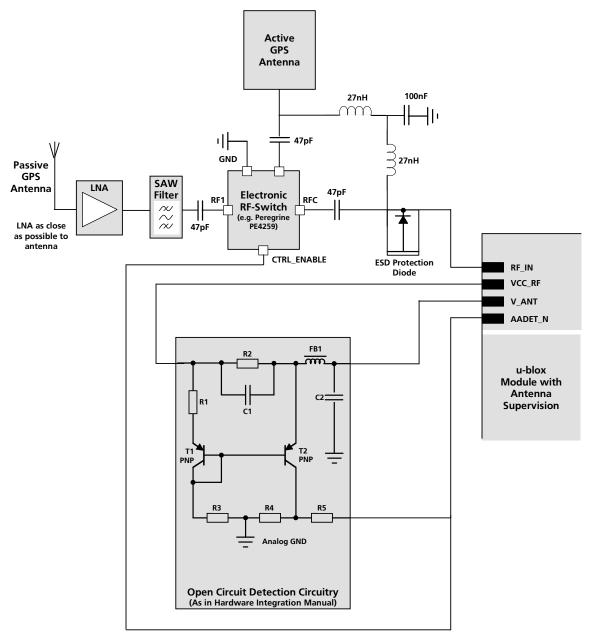


Figure 26: Example of dual antenna system using an electronic switch.

As an alternative to the solution shown in Figure 26 a mechanical switch can also be used (see Figure 27). For values of the components identified see the Integration Manual for the specific u-blox receiver.



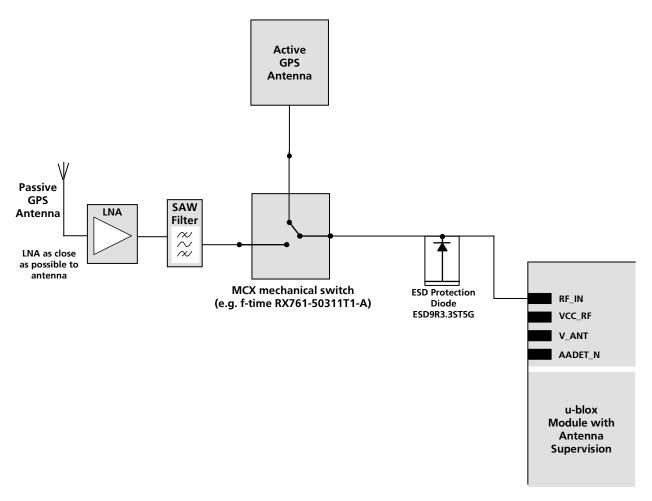


Figure 27: Example of dual antenna system using a mechanical switch.



6 Interference issues

A typical GPS/GALILEO receiver has a very low dynamic range. This is because the antenna should only detect thermal noise in the GPS/GALILEO frequency band, given that the peak power of the GPS/GALILEO signal is 15 dB below the thermal noise floor. This thermal noise floor is usually very constant over time. Most receiver architectures use an automatic gain control (AGC) circuitry to automatically adjust to the input levels presented by different antenna and pre-amplifier combinations. The control range of these AGC's can be as large as 50 dB. However, the dynamic range for a jamming signal exceeding the thermal noise floor is typically only 6 to 12dB, due to the one or two bit quantization schemes commonly used in GPS/GALILEO receivers. If there are jamming signals present at the antenna and the levels of these signals exceed the thermal noise power, the AGC will regulate the jamming signal, suppressing the GPS/GALILEO signal buried in thermal noise even further. Depending on the filter characteristics of the antenna and the front end of the GPS/GALILEO receiver, the sensitivity to such in-band jamming signals decreases more or less rapidly if the frequency of the jamming signal moves away from GPS/GALILEO signal frequency. We can conclude that a jamming signal exceeding thermal noise floor within a reasonable bandwidth (e.g. 100 MHz) around GPS/GALILEO signal frequency will degrade the performance significantly.

Even out-of-band signals can affect GPS/GALILEO receiver performance. If these jamming signals are strong enough that even antenna and front-end filter attenuation are not sufficient, the AGC will still regulate the jamming signal. Moreover, very high jamming signal levels can result in non-linear effects in the pre-amplifier stages of the receiver, resulting in desensitizing of the whole receiver. One such particularly difficult scenario is the transmitting antenna of a DCS⁴ handset (max. 30 dBm at 1710 MHz) in close proximity to the GPS/GALILEO antenna. When integrating GPS/GALILEO with other RF transmitters special care is necessary.

If the particular application requires integration of the antenna with other digital systems, one should make sure that jamming signal levels are kept to an absolute minimum. Even harmonics of a CPU clock can reach as high as 1.5 GHz and still exceed thermal noise floor.

On the receiver side there's not much that can be done to improve the situation without significant effort. Of course, high price military receivers have integrated counter-measures against intentional jamming. But the methods employed are out of the scope of this document and might even conflict with export restrictions for dual-use goods.

The recommendations and concepts in this section are completely dependent on the specific applications. In situations where an active antenna is used in a remote position, e.g. >1 m away from other electronics, interference should not be an issue.



If antenna and electronics are to be tightly integrated, the following sections should be read very carefully.

6.1 Sources of noise

Basically two sources are responsible for most of the interference with GPS receivers:

- 1. Strong RF transmitters close to GPS frequency, e.g. DCS at 1710 MHz or radars at 1300 MHz.
- 2. Harmonics of the clock frequency emitted from digital circuitry.

The first problem can be very difficult to solve, but if GPS/GALILEO and RF transmitter are to be integrated close to each other, there's a good chance that there is an engineer at hand who knows the specifications of the RF transmitter. In most cases, counter measures such as filters will be required for the transmitter to limit disruptive emissions below the noise floor near the GPS/GALILEO frequency.

Even if the transmitter is quiet in the GPS/GALILEO band, a very strong emission close to it can cause saturation in the front-end of the receiver. Typically, the receiver's front-end stage will reach its compression point, which will in turn increase the overall noise figure of the receiver. In that case, only special filtering between the

⁴ Digital Communication System



GPS/GALILEO antenna and receiver input will help to reduce signal levels to the level of linear operation at the front-end.

The second problem is more common but also regularly proves to be hard to solve. Here, the emitting source is not well specified and the emission can be of broadband nature, making specific countermeasures very difficult. Moreover, the GPS/GALILEO band is far beyond the 1 GHz limit that applies to almost all EMC regulations. So, even if a device is compliant with respect to EMC regulations it might severely disturb a GPS receiver.

If the GPS/GALILEO antenna is to be placed very close to some other electronics, e.g. the GPS/GALILEO receiver itself or a PDA-like appliance, the EMC issue must be taken very seriously right from the concept phase of the design. It is one of the most demanding tasks in electrical engineering to design a system that is essentially free of measurable emissions in a given frequency band.

6.2 Eliminating digital noise sources

Digital noise is caused by short rise-times of digital signals. Data and address buses with rise-times in the nanosecond range will emit harmonics up to several GHz. The following sections contain some general hints on how to decrease the level of noise emitted from digital circuit board that are potentially in close proximity to the GPS receiver or the antenna.

6.2.1 Power and ground planes

Use solid planes for power and ground interconnect. This will typically result in a PCB with at least four layers but will also result in a much lower radiation. Solid ground planes ensure that there is a defined return path for the signals routed on the signal layer. This will reduce the "antenna" area of the radiating loop. Planes should be solid in a sense that there are no slots or large holes inside the plane.

The outer extent of the power plane should be within the extent of the ground plane. This avoids that the edges of the two planes form a slot antenna at the board edges. It's a good idea to have a ground frame on the circumference of every layer that is connected to the ground plane with as many vias as possible. If necessary, a shield can then be easily mounted on top of this frame (see Figure 28). Furthermore, free space on the outermost Layers can be filled with ground shapes connected to the ground plane to shield radiation from internal layers.

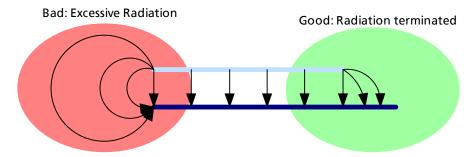


Figure 28: Signal and power plane extends should lie within ground plane extends

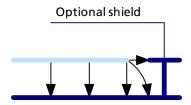


Figure 29: Further improvement of reduction of power plane radiation



6.2.2 High speed signal lines

Keep high-speed lines as short as possible. This will reduce the area of the noise-emitting antenna, i.e. the conductor traces. Furthermore, the use of line drivers with controlled signal rise-time is suggested whenever driving large bus systems. Alternatively, high-speed signal lines can be terminated with resistors or even active terminations to reduce high frequency radiation originating from overshoot and ringing on these lines.

If dielectric layers are thick compared to the line width, route ground traces between the signal lines to increase shielding. This is especially important if only two layer boards are used (see Figure 30).

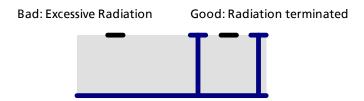


Figure 30: Terminating radiation of signal lines

6.2.3 Decoupling capacitors

Use a sufficient number of decoupling capacitors in parallel between power and ground nets. Small size, small capacitance types reduce high-frequency emissions. Large size, high capacitance types stabilize low frequency variations. It's preferred to have a large number of small value capacitors in parallel rather than having a small number of large value capacitors. Every capacitor has an internal inductance in series with the specified capacitance. In addition to resonance, the capacitor will also behave like an inductor. If many capacitors are connected in parallel, total inductance will decrease while total capacitance will increase. Figure 31 shows the impedance dependence of SMD capacitors.

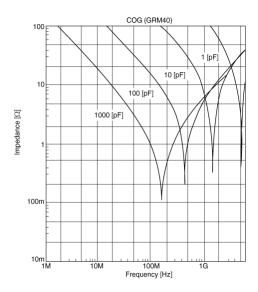


Figure 31: Impedance of 0805 size SMD capacitors vs. frequency, MuRata

If the power and ground plane are not connected by an efficient capacitor network, the power plane may act as a radiating patch antenna with respect to the ground. Furthermore, ceramic capacitors come with different dielectric materials. These materials show different temperature behavior. For industrial temperature range applications, at least a X5R quality should be selected. Y5V or Z5U types may lose almost all of their capacitance at extreme temperatures, resulting in potential system failure at low temperatures because of excessive noise



emissions from the digital part. Tantalum capacitors show good thermal stability, however, their high ESR (equivalent series resistance) limits the usable frequency range to some 100 kHz.

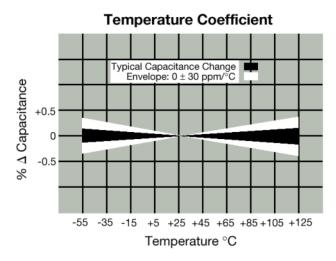


Figure 32: Temperature dependency of COG/NPO dielectric, AVX

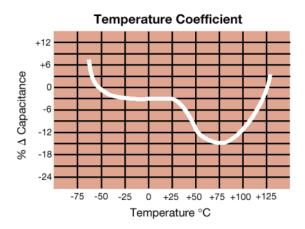


Figure 33: Temperature dependency of X7R dielectric, AVX

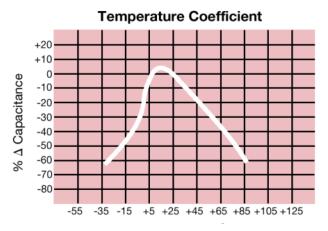


Figure 34: Temperature dependency of Y5V dielectric, AVX



6.3 Shielding

If employing countermeasures cannot solve EMI problems, the solution may be shielding of the noise source. In the real world, shields are not perfect. The shielding effectiveness you can expect from a solid metal shield is somewhere in the order of 30-40 dB. If a thin PCB copper layer is used as a shield, these values can be even lower. Perforation of the shield will also lower its effectiveness.

Be aware of the negative effects that holes in the shield can have on shielding effectiveness. Lengthy slots might even turn a shield into a radiating slot antenna. Therefore, a proper shield has to be tightly closed and very well connected to the circuit board.

6.3.1 Feed through capacitors

The basic concept of shielding is that a metal box will terminate all electrical fields on its surface. In practice we have the problem that we need to route some signals from inside to outside of this box.

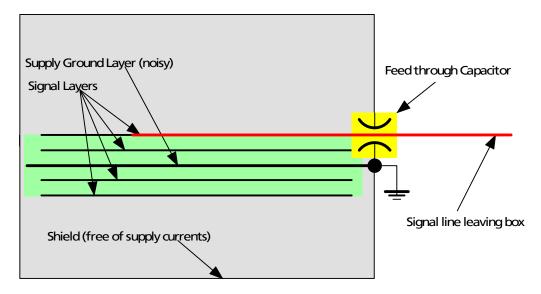


Figure 35: Ideal shielding

The proposed setup for such a system is shown in Figure 35. A feed through capacitor removes all high frequency content from the outgoing signal line. It's important to note that any conductor projecting through the shielding box is subject to picking up noise inside and re-radiating it outside, regardless of the actual signal it is intended to carry. Therefore, also DC lines (e.g. the power supply) should be filtered with feed through capacitors. When selecting feed through capacitors, it's important to choose components with appropriate frequency behavior. As with the ordinary capacitors, small value types will show better attenuation at high frequencies (see Figure 36). For the GPS/GALILEO frequency band the 470pF capacitor is the optimum choice of the Murata NFM21C series.



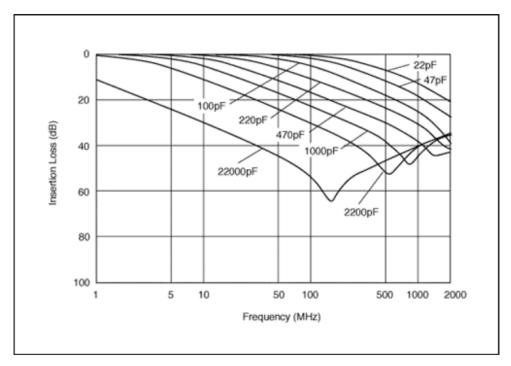


Figure 36: MuRata's NFM21C feed through capacitors

Any feed through capacitor will only achieve its specified performance if it has a proper ground connection.

If the use of a special feed through capacitor is not feasible for a particular design, a simple capacitor between the signal line and shielding ground placed very close to the feed through of the signal line will also help. It has been found that a 12 pF SMD capacitor works quite well at the GPS/GALILEO frequency range. Larger capacitance values will be less efficient.

One should keep in mind that a feed through capacitor is basically a high frequency "short" between the signal line and ground. If the ground point that the capacitor is connected to is not ideal, meaning the ground connection or plane has a finite resistance, noise will be injected into the ground net. Therefore, one should try to place any feed trough capacitor far away from the most noise sensitive parts of the circuit. To emphasize this once again, one should ensure a very good ground connection for the feed through capacitor.

If there is no good ground connection available at the point of the feed through, or injection of noise into the non-ideal ground net must be avoided totally, inserting a component with a high resistance at high frequencies might be a good alternative. Ferrite beads are the components of choice if a high DC resistance cannot be accepted. Otherwise, for ordinary signal lines one could insert a 1 K series resistor, which would then form a low-pass filter together with the parasitic capacitance of the conductor trace.

See also the MuRata web page for extensive discussion on EMC countermeasures.



6.3.2 Shielding sets of sub-system assembly

Yet another problem arises if multiple building blocks are combined in a single system. Figure 37 shows a possible scenario. In this case, the supply current traveling through the inductive ground connection between the two sub-systems will cause a voltage difference between the two shields of the sub-system. The shield of the other system will then act as a transmitting antenna, radiating with respect to the ground and shield of the GPS/GALILEO receiver and the attached antenna.

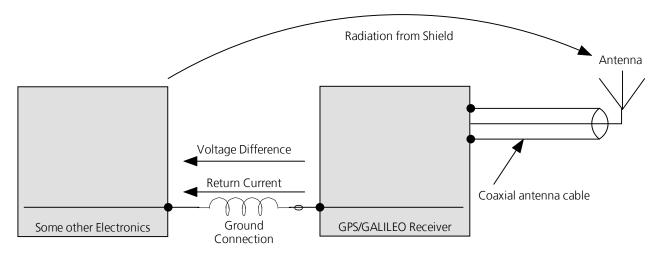


Figure 37: Two shielded sub-systems, connected by a "poor" ground

This situation can be avoided by ensuring a low inductivity ground connection between the two shields. But now, it might be difficult to control the path of the ground return currents to the power supply since the shield is probably connected to the supply ground at more than one location. The preferred solution is shown in Figure 38. Again, it is important to have a good (i.e. low inductance) interconnection between the outer shield and the shielding ground of the GPS/GALILEO receiver.

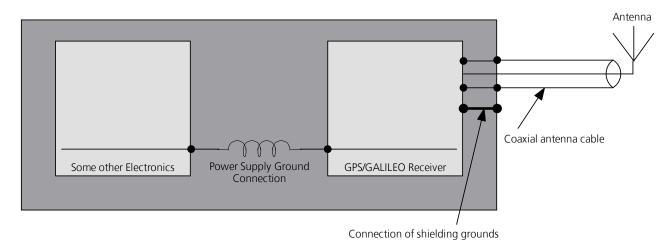


Figure 38: Proper shielding of a sub-system assembly

It is clear that the situation illustrated in Figure 38 can become complex if the component "Some other electronics" contains another wireless transmitter system with a second antenna, which is referenced to the systems shielding ground. As already pointed out, in a setup like this it is important to keep the shield free from supply currents with high frequency spectral content. If there are to be additional connections to the shielding ground, these should be of a highly inductive nature.



6.4 Increasing jamming immunity

Jamming signals come from in-band and out-band frequency sources.

6.4.1 In-band jamming

With in-band jamming the signal frequency is very close to the GPS frequency of 1575 MHz. Such jamming signals are typically caused by harmonics from displays, micro-controller, bus systems, etc.

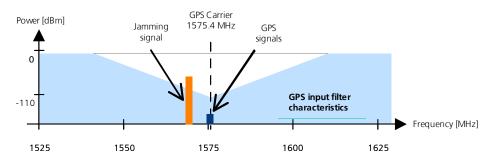


Figure 39: In-band jamming signals

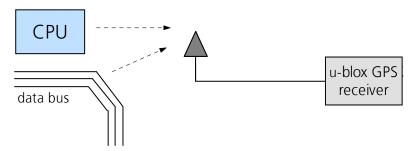


Figure 40: In-band jamming sources

Measures against in-band jamming include:

- Maintaining a good grounding concept in the design
- Shielding
- Layout optimisation
- Filtering
- Placement of the GPS antenna
- Adding a CDMA, GSM, WCDMA bandbass filter before handset antenna

6.4.2 Out-band jamming

Out-band jamming is typically caused by signal frequencies that are different from the GPS carrier. The sources are usually wireless communication systems such as GSM, CDMA, WCDMA, WiFi, BT, etc..



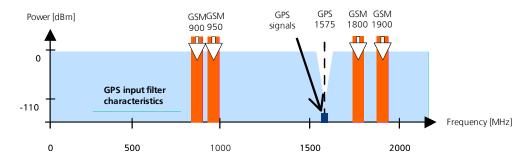


Figure 41: Out-band jamming signals

Measures against out-band jamming include maintaining a good grounding concept in the design and adding a SAW or bandpass ceramic filter into the antenna input line to the GPS receiver (see Figure 42).



Figure 42: Measures against in-band jamming



7 Performance tests

For successful integration of a GPS design, it is recommended to perform outdoor static measurements (with measurements performed in a defined location and with good sky visibility). u-center is the ideal tool for the design verification. The Sky View and the Statistic View functions are especially helpful. See the u-blox website to download u-center.

7.1 Sky view

The "Sky View" tool of the u-center software is an excellent means to analyze antenna performance as well as the environmental conditions for satellite observation. The polar plot graphically displays the average satellite signal strength, the position of satellites in the sky, identifies satellites by number and indicates which satellites are being used.

When recording over a long time period, "sky view" is excellent for displaying the antenna visibility and to perform a comparison between two designs. Regarding the distribution of GPS satellites, customers should conduct at least 12-hour measurements to have a 360-degree antenna visibility view with their design. Record a second 12-hour test with the design turned by 180 degree on the horizontal plane. Compare the recorded files for both designs for the north and the south hemisphere.

The following pictures show 24-hour outdoor measurements done with the design. While the picture on the left side shows mediocre performance (recommendations were not followed), the one on the right side shows successful integration of the design.

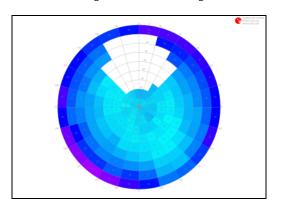


Figure 43: 24-Hour Sky view test with poor Antenna

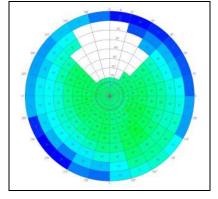


Figure 44: 24-Hour Sky view test with a standalone reference

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(8)

These pictures show examples of possible measurements. Bear in mind that the plots might be influenced by the environment (e.g. buildings or hills close by) as well as GPS conditions (e.g. no satellites above the north and south pole).

7.2 Statistic view

The u-center "statistic view" values displayed in the table below can be easily copied from the u-center Software and pasted in an Excel sheet for comparison purposes.

Title	Current	Minimum	Maximum	Average	Deviation	Unit
SVs Used	6	5	11	8	1	
Used SVs	3,11,14,21,28,31					
SVs Tracked	7	7	12	9	1	
Tracked SVs	3,11,14,20,21,28,31					
SV C/NO	42.13	35.7	44.5	41.56	1.06	dBHz

Table 3: Example of statistic view



Average, minimum and maximum signal strength ratios, so called C/No (carrier to noise ratio), provide good estimates of the signal reception quality.

7.3 Supply voltage check

Once a design prototype is ready the supply voltage ripple at the module should be checked. It must be below 50 mV p-p.

- Place the LDO for the GPS close to the receiver.
- Use wide PCB traces or power planes.
- Ensure that there are no inductors, resistors, fuses or diodes between the LDO and the GPS receiver.

7.4 Sensitivity test

Check the C/No values in the \$GPGSV or the UBX-NAV-SVINFO messages. Under open sky a good design should reach up to 50 dBHz for the strongest signals. If it reaches 45dBHz it can still be acceptable but the source of the reduction should be investigated (e.g. small antenna, ...).

Designs with maximal signal strengths below 40dBHz usually provide degraded performance (long TTFF times, lower coverage, accuracy, dynamic).

7.5 Startup test

With good sky visibility the device should make a coldstart within 30 - 40 seconds. (Take the average of 10-20 tests).



Appendix

A Example of receiver signal to noise (C/No) performance calculation

Figure 45 shows a typical GPS receiver system setup. It is apparent that the noise figure of the first LNA (NF₁) will dominate the receiver noise performance. If the first LNA is absent, the losses between antenna and receiver input should be minimal. These losses are represented by gain (G₁) and NF₁ (G₁ < 1!). If the cable losses are reasonably low, the first amplifier stage of the GPS receiver (LNA₂) dominates the noise performance of the system. Even the best receiver can't compensate the cable losses, in best cases it will only add no further losses.

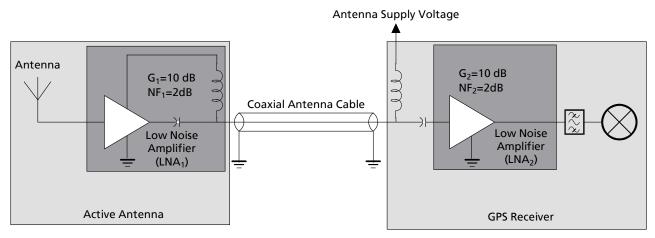


Figure 45: GPS receiver setup with antenna

Table 4 shows an example calculation for a typical GPS receiver processing chain as depicted in Figure 45. The final stage of the receiver is summarized in the row "Receiver". Gain and noise figure are arbitrarily chosen since this figure will not include the transition to the digital signal processing domain. To the user of a GPS RF chip the internal analog noise figure of the receiver is usually not visible, because he can only access the digitized output signals of this stage. If quantizer architecture and gain control mechanisms are known, one can however reverse calculate the noise figure of the analog part. Finally, in digital signal processing there are noise contributions which do not fit into the concept of noise figure theory. To just name one, quantization errors will always contribute a fixed amount of noise, regardless of the signal pre-amplification.

J 3 .	Gain of	Power	Figure of	Noise		Noise Power Density	C/No
	[dB]	[dB]	[dB]	[dB]	[dBm]	[dBm/Hz]	[dBHz]
Antenna				0.00	-130	-173.98	43.98
Cable	-1	-1	1	1.00	-131	-173.98	42.98
1st LNA	10	9	2	3.00	-121	-161.98	40.98
Cable	-3	6	3	3.26	-124	-164.71	40.71
2nd LNA	10	16	2	3.56	-114	-154.42	40.42
Receiver	60	76	10	3.97	-54	-94.01	40.01

Table 4: Example of Receiver Gain and Noise calculation



The highlighted fields in Table 4 are the input values, the other fields show the results calculated from these numbers. For a higher gain antenna, one could increase the carrier power in the first line by the antenna gain given in dBic. One could also make a different assumption for the antenna noise temperature in the first line and change the initial noise power density accordingly. Of course, gain and noise figure of the analog signal processing stage differ for every receiver implementation.

Figure 46 illustrates the direct relation between receiver noise figure and the C/N_0 measure that is available to the receiver. Again, one can easily see the dominant influence of the first processing stages. If the first LNA would have a higher gain, e.g. 25 dB instead of only 10 dB, the contribution of the subsequent stages to the sensitivity degradation would be negligible.

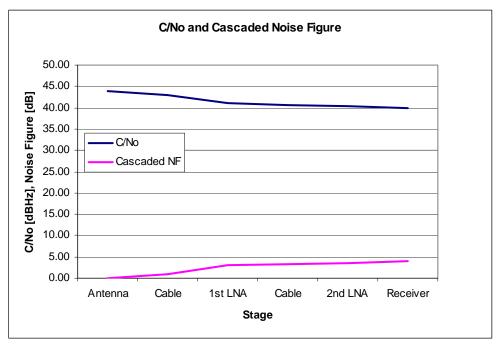


Figure 46: C/No and noise figure in receiver processing chain



Related documents

- [1] GPS Compendium, Docu. No GPS-X-02007
- [2] u-blox 5 Receiver Description including Protocol Specification, Docu. No GPS.G5-X-07003

All these documents are available on our homepage (http://www.u-blox.com).



For regular updates to u-blox documentation and to receive product change notifications please register on our homepage.

Revision history

Revision	Date	Name	Status / Comments
-	29/01/2009	jfuh, tgri	Initial release
A1	02/07/2009	jfuh, tgri	New CI, 2.2 Antenna, 2.3 Dipole antenna, 5.1.2 Placement, 5.1.3 ESD protection measures, 5.5 Mechanical dual antenna system solution, 6.4 Increasing jamming immunity, Appendix



Contact

For complete contact information visit us at www.u-blox.com

u-blox Offices

North, Central and South America

u-blox America, Inc.

Phone: +1 (703) 483 3180 E-mail: info_us@u-blox.com

Regional Office West Coast:

Phone: +1 (703) 483 3184 E-mail: info_us@u-blox.com

Technical Support:

Phone: +1 (703) 483 3185 E-mail: support_us@u-blox.com

Headquarters Europe, Middle East, Africa

u-blox AG

Phone: +41 44 722 74 44
E-mail: info@u-blox.com
Support: support @u-blox.com

Asia, Australia, Pacific

u-blox Singapore Pte. Ltd.

Phone: +65 6734 3811 E-mail: info_ap@u-blox.com Support: support_ap@u-blox.com

Regional Office China:

Phone: +86 10 68 133 545 E-mail: info_cn@u-blox.com Support: support_cn@u-blox.com

Regional Office Japan:

Phone: +81 3 5775 3850 E-mail: info_jp@u-blox.com Support: support_jp@u-blox.com

Regional Office Korea:

Phone: +82 2 542 0861 E-mail: info_kr@u-blox.com Support: support_kr@u-blox.com

Regional Office Taiwan:

Phone: +886 2 2657 1090 E-mail: info_tw@u-blox.com Support: support_tw@u-blox.com